



THTMD
THAILAND'S HUB OF TALENTS IN MICROELECTRONIC DESIGN
CRAFTING CONNECTIONS

The First International Symposium on Microelectronic Design
National Research Council of Thailand (NRCT)
Thailand Hub of Talents in Microelectronic Design (THTMD)
Nokyoong room, Empress Premier Hotel Chiang Mai, Thailand
11 - 13 December 2024

Time	Program
11 December 2024	
08.00 – 09.00 am	Registration
09.00 – 09.15 am	Welcoming Speech Dr. Wiparat De-ong Executive Director National Research Council of Thailand
09.15 – 09.30 am	Opening Ceremony Ms. Sunee Lertphienthum Chief of General Inspector Acting Deputy Permanent Secretary Ministry of Higher Education, Science, Research and Innovation
09.30 – 10.30 am	Implantable Integrated Circuits and Systems for Neurostimulation and Neuromodulation Professor Dr.-Ing. Maurits Ortmanns Institute of Microelectronics, University of Ulm
10.30 – 10.45 am	Morning Break
10.45 – 11.25 am	IC chip technology development and talent cultivation in Taiwan Dr. Ying-Zong Juang Deputy Director General, TSRI
11.25 am – 12.05 pm	Design Challenges of Antennas on Chip and Antenna in Package Assoc. Prof. Dr. Suramate Chalermwisutkul Thai-German Graduate School of Engineering (TGGS), King Mongkut's University of Technology North Bangkok
12.05 – 01.30 pm	Lunch Break



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01.30 – 02.30 pm	Machine Learning for the Design of MEMS Devices - ONLINE - Professor Dr. Michael Kraft Department of Electrical Engineering, KU Leuven
02.30 – 03.10 pm	Self-Powered Power Management Circuits for Piezoelectric Energy Harvesting Associate Professor Dr. Shu-Chuan Huang Department of Electrical Engineering, Tatung University, Taiwan
03.10 – 03.25 pm	Afternoon Break
03.30 – 04.00 pm	Topic: -TBA- Assoc. Prof. Dr. Woradorn Wattanapanitch Department of Electrical Engineering, Kasetsart University
05.30 – 06.30 pm	Transfer from hotel to Khantoke Dinner at Khum KhanToke, Chiang Mai
07.00 – 08.30 pm	Welcome Party - Khantoke Dinner -
08.30 – 09.00 pm	Transfer from Khantoke Dinner to hotel



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12 December 2024	
09.00 – 10.00 am	Experience sharing of chip implementation services and CMOS MEMS technology at TSRI Dr. Sheng-Hsiang Tseng Division Director, Chip Implementation Service Division, TSRI
10.00 – 10.15 am	Morning Break
10.15 – 10.55 am	Ultra-low-power voltage reference design: challenges and opportunities Dr. Chutham Sawigun Senior Researcher, Neurotech ASIC Design, imec, Belgium
10.55 – 11.35 am	Integrated Passive Device Design for Wireless Communication Assoc. Prof. Dr. Chatrpol Pakasi King Mongkut's Institute of Technology Ladkrabang
11.35 – 12.15 pm	Low Noise Analog Front End Design for Biomedical Signal Assoc. Prof. Dr. Surachoke Thanapitak Mahidol University
12.15 – 01.30 pm	Lunch Break
01.30 – 02.10 pm	Next-Generation Devices: Advances in SiGe Nanosheet Technology and Monolayer Doping Assoc. Prof. Dr. Shu-Han Hsu Sirindhorn International Institute of Technology, Thammasat University



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02.10 – 02.45 pm	Powering Innovation in Microelectronics and Semiconductor with Engineering Simulation Mr.Satish Screenivasulu (Electronics and Semiconductor Technology Specialist) CADFEM (Thailand) Co., Ltd.
02.45 – 03.00 pm	Afternoon Break
03.00 – 04.00 pm	CMOS Ion-Sensing Arrays enabling Rapid Diagnostics and Surveillance for Infectious Diseases - ONLINE - Professor Dr. Pantelis Georgiou Faculty of Engineering, Imperial College London
04.00 – 05.00 pm	Poster Section
06.00 – 08.00 pm	Banquet at THE POOL, Empress Premier Hotel Chiang Mai

Time	Program
13 December 2024	

09.00 am – 04.00 pm Networking and Culture Activity